

Nume Prenume: Chindris Gabriel
Gradul didactic: Conferentiar Universitar

Instituția unde este titular: **Universitatea Tehnică din Cluj-Napoca**
Facultatea: Electronica, Telecomunicatii si Tehnologia Informatiei
Departamentul: Electronica Aplicata

L I S T A

lucrărilor științifice în domeniul disciplinelor din postul didactic

A. Teza de doctorat

1. „Contributii la dezvoltarea tehnologiilor digitale pentru sistemele de putere”
Conducător științific : Prof.dr.ing. Dan PITICA
Domeniul Inginerie Electronica
Universitatea Tehnica din Cluj-Napoca
Susținere publică: 29.06.2008.

B. Cărți si capitole în cărți publicate în ultimii 10 ani

1. Alin Grama, **Gabriel Chindris** - *Sisteme Embedded - Proiectare hardware si software* – Editura Mediamira, 2015, Cluj-Napoca, ISBN: 978-973-713-354-0, 140 pagini;
2. Rajmond Jano, **Gabriel Chindris** - *Hardware and Software Testing Principles: Hands-on Training Guide* - Editura Mediamira, 2016, Cluj-Napoca, ISBN 978-973-713-345-8, 120 pagini;
3. Ovidiu Pop, Raul Fizesan, Gabriel Chindris - *Proiectare asistata de calculator – aplicatii* - Editura UTPress, 2013, Cluj-Napoca, ISBN 978-973-662-856-6, 140 pagini;
4. Ovidiu Pop, Raul Fizesan, Gabriel Chindris - *Computer Aided Design-Laboratory Applications* - Editura UTPress, 2013, Cluj-Napoca, ISBN 978-973-662-856-6, 140 pagini;
5. Adrian Taut, Gabriel Chindris, Ovidiu Aurel Pop - *Grafica Asistata de Calculator - Note de curs si aplicatii* - Editura UTPress, 2013, Cluj-Napoca, ISBN 978-973-662-856-6, 140 pagini;

C. Lucrări indexate ISI/BDI publicate în ultimii 10 ani

- 1) Taut, M.A., **Chindriș, G.**, Pitica, D. - Real-Time System with Integrated PID Algorithm Used for DC Motor Control - (2019) Proceedings of the International Spring Seminar on Electronics Technology, 2019-May, art. no. 8810310, DOI: 10.1109/ISSE.2019.8810310 [IEEE][SCOPUS]
- 2) Fodor, A., **Chindriș, G.**, Jano, R., Pitica, D. - Thermal Modelling and Simulation Techniques for Multicore Processors - (2019) Proceedings of the International Spring

Seminar on Electronics Technology, 2019-May, art. no. 8810200, DOI: 10.1109/ISSE.2019.8810200 [IEEE][SCOPUS]

- 3) Taut, M.A., **Chindriș, G.**, Pitică, D. - PID Algorithm used for DC Motor Control - (2019) 2018 IEEE 24th International Symposium for Design and Technology in Electronic Packaging, SIITME 2018 - Proceedings, art. no. 8599230, pp. 365-372. DOI: 10.1109/SIITME.2018.8599230 [IEEE][SCOPUS]
- 4) Taut, M.A., **Chindriș, G.**, Taut, A.C., Pitica, D. - Model-in-the-Loop for Determining the Speed and Position of a DC Motor - (2018) Proceedings of the International Spring Seminar on Electronics Technology, 2018-May, art. no. 8443767, . DOI: 10.1109/ISSE.2018.8443767 [IEEE][SCOPUS]
- 5) Daraban, M., **Chindriș, G.**, Taut, A., Viman, L. - Uncertainty Budget for Hardware-in-the-Loop Test System - (2018) Proceedings of the International Spring Seminar on Electronics Technology, 2018-May, art. no. 8443692, . DOI: 10.1109/ISSE.2018.8443692 [IEEE][SCOPUS]
- 6) Taut, A., **Chindriș, G.**, Daraban, M., Taut, M. - Resonant Power Converters used for Wireless Power Transfer - (2018) Proceedings of the International Spring Seminar on Electronics Technology, 2018-May, art. no. 8443677, . DOI: 10.1109/ISSE.2018.8443677 [IEEE][SCOPUS]
- 7) Tăut, M.A., **Chindriș, G.**, Pitică, D., Fodor, A. - Model-in-the-Loop for determining the parameters of a DC motor - (2017) 2017 IEEE 23rd International Symposium for Design and Technology in Electronic Packaging, SIITME 2017 - Proceedings, 2018-January, pp. 267-273. DOI: 10.1109/SIITME.2017.8259906 [IEEE][SCOPUS]
- 8) Fodor, A., Jánó, R., **Chindriș, G.**, Pitica, D. - Thermal via placement for high-power applications - (2017) 2017 IEEE 23rd International Symposium for Design and Technology in Electronic Packaging, SIITME 2017 - Proceedings, 2018-January, pp. 223-226. DOI: 10.1109/SIITME.2017.8259895 [IEEE][SCOPUS]
- 9) Fodor, A., **Chindriș, G.**, Pitica, D. - Enhancing thermal capabilities of component packaging - (2016) 2016 IEEE 22nd International Symposium for Design and Technology in Electronic Packaging, SIITME 2016, art. no. 7777264, pp. 142-145. DOI: 10.1109/SIITME.2016.7777264 [IEEE][SCOPUS]
- 10) Fodor, A., **Chindriș, G.**, Pitica, D., Jano, R. - Task allocation for thermal optimization in multicore systems - (2016) Proceedings - 2016 IEEE 12th International Conference on Intelligent Computer Communication and Processing, ICCP 2016, art. no. 7737172, pp. 349-352 DOI: 10.1109/ICCP.2016.7737172 [IEEE][SCOPUS]
- 11) Miclăuș, A., Jánó, R., **Chindriș, G.** - Implementation of a car model for indirect tire pressure monitoring system - (2016) Proceedings of the International Spring Seminar on Electronics Technology, 2016-September, art. no. 7563212, pp. 316-321. DOI: 10.1109/ISSE.2016.7563212 [IEEE][SCOPUS]

- 12) Ilieş, A.I., Jánó, R., **Chindriş, G.** - Thermal management of System-on-Chip devices used in satellite systems under low temperature conditions - (2016) Proceedings of the International Spring Seminar on Electronics Technology, 2016-September, art. no. 7563168, pp. 91-96. DOI: 10.1109/ISSE.2016.7563168 [IEEE][SCOPUS]
- 13) Fodor, A., **Chindriş, G.**, Pitica, D., Jano, R. - Guidelines on thermal management solutions for modern packaging technologies - A review - (2015) 2015 IEEE 21st International Symposium for Design and Technology in Electronic Packaging, SIITME 2015, art. no. 7342292, pp. 41-44. DOI: 10.1109/SIITME.2015.7342292 [IEEE][SCOPUS]
- 14) Baciú, I.H., Taut, A., **Chindriş, G.**, Fodor, A. - Mathematical model for a quasi-resonant converter - (2015) 2015 IEEE 21st International Symposium for Design and Technology in Electronic Packaging, SIITME 2015, art. no. 7342326, pp. 213-216. DOI: 10.1109/SIITME.2015.7342326 [IEEE][SCOPUS]
- 15) Taut, A., Daraban, M., Pop, O., **Chindriş, G.**, Fizesan, R. - Mathematical analysis to control power transfer in resonant power converters - (2015) 2015 IEEE 21st International Symposium for Design and Technology in Electronic Packaging, SIITME 2015, art. no. 7342335, pp. 253-256. DOI: 10.1109/SIITME.2015.7342335 [IEEE][SCOPUS]
- 16) Baciú, I.H., Viman, L., Fodor, A., **Chindriş, G.** - Advanced methods of generating signals to command switching converters - (2013) Proceedings of the International Spring Seminar on Electronics Technology, art. no. 6648242, pp. 202-205. Cited 1 time. DOI: 10.1109/ISSE.2013.6648242 [IEEE][SCOPUS]
- 17) Dachin, T., Pitica, D., **Chindriş, G.** - Shunt less current monitoring technique: Alternative current measurement method, voltage drop on wires due to supply variation - (2013) 2013 IEEE 19th International Symposium for Design and Technology in Electronic Packaging, SIITME 2013 - Conference Proceedings, art. no. 6743672, pp. 197-200. DOI: 10.1109/SIITME.2013.6743672 [IEEE][SCOPUS]
- 18) M Daraban, C Corches, R Fizesan, **G Chindris** - Real-Time Embedded Framework Debugger - 2022 IEEE 28th International Symposium for Design and Technology in Electronic Packaging (SIITME)
- 19) Alexandra Fodor, Adrian Taut, **Gabriel Chindris** - Increasing Thermal Simulation Efficiency with 6SigmaET - 2021 IEEE 27th International Symposium for Design and Technology in Electronic Packaging (SIITME)
- 20) Mihai Daraban, Cosmina Corches, Adrian Taut, **Gabriel Chindris** - Protocol over UART for Real-Time Applications - 2021 IEEE 27th International Symposium for Design and Technology in Electronic Packaging (SIITME)

- 21) A Fodor, **G Chindris** - Estimating Power Dissipation through Thermal Measurements in Power Circuits - 2020 IEEE 26th International Symposium for Design and Technology in Electronic Packaging (SIITME)
- 22) Adelina Ioana Ilies, Dan Pitica, **Gabriel Chindris**, Alexandra Fodor - Test Bench for Electrical and Performance Evaluation of Lithium-Ion Batteries - 2019 IEEE 25th International Symposium for Design and Technology in Electronic Packaging (SIITME)
- 23) MA Taut, **G Chindris**, AC Taut - Software-in-the-Loop System for Motor Control Algorithms - 2019 IEEE 25th International Symposium for Design and Technology in Electronic Packaging (SIITME)

D. Lucrări publicate în ultimii 10 anii în reviste și volume de conferințe cu referenți (neindexate)

- Reviste

1.

- Selecție cu maximum 20 lucrări în volume de conferințe

1.

E. Brevete obținute în întreaga activitate

1. CM Amariei, **G Chindris**, D Cosovanu - Computer-implemented method, wearable device, computer program and computer readable medium for assisting the movement of a visually impaired user – 2022 US Patent 11,371,859

Data:

06.02.2025

E – Brevete (pentru întreaga activitate)

Semnătura:

